



Image

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/062,995
	Filing Date	1/31/02
	First Named Inventor	Steven Teig
	Group Art Unit	2825
	Examiner Name	Rossoshek, Yelena
Total Number of Pages in This Submission		Attorney Docket Number SPLX.P0105

Enclosures (Check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm Or Individual name	Mani Adeli Reg. No. 39,585 of Stattler Johansen & Adeli LLP
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Date	January 22, 2004

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PATENT
Docket No. SPLX.P0105

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Mani Adeli

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Steven Teig, et al.

Serial No.: 10/062,995

Filing Date: 1/31/02

For: POLYGONAL VIAS

**INFORMATION DISCLOSURE
STATEMENT UNDER 37 C.F.R. § 1.97**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. § 1.97 and § 1.98, applicants submit with this Information Disclosure Statement the attached Form PTO-1449, and copies of the documents listed in the 1449 form for consideration by the Examiner. The Examiner is requested to make these documents of record. Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

This Information Disclosure Statement under 37 C.F.R. § 1.97 is not to be construed as a representation that: (i) a complete search has been made; (ii) additional information material to the examination of this application does not exist; (iii) the information, protocols, results and the

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like reported by third parties are accurate or enabling; or (iv) the above information constitutes prior art to the subject invention.

This Information Disclosure Statement is submitted after receipt of a first Office Action on the merits but before the Final Office Action. Accordingly, a fee is required. The requisite fee is attached.

However, in the unlikely event that the Patent Office determines that additional fees, extension and/or other relief is required, applicant petitions for any required relief including extensions of time and authorizes the Assistant Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to **Deposit Account No. 50-1128** referencing SPLX.P0105. However, the Assistant Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

Dated: January 22, 2004

Respectfully submitted,

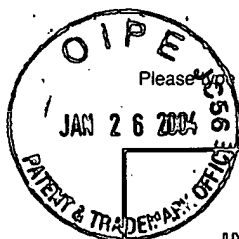
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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	10/062,995
				Filing Date	1/31/02
				First Named Inventor	Steven Teig, et al.
				Group Art Unit	2825
				Examiner Name	Rossoshek, Yelena
Sheet	1	of	4	Attorney Docket Number	SPLX.P0105

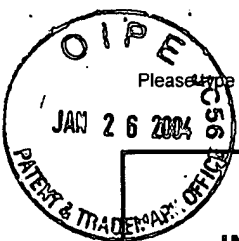
U.S. PATENT DOCUMENTS						
Examiner* Initials	Cite No. ¹	U.S. Patent Document		Date of Publication MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	1.	2001/0003 843	A1	06-14-2001	Scepanovich et al.	
	2.	2002/0182 844	A1	12-05-2002	Igarashi et al.	
	3.	2003/0053 99	A1	01-02-2003	Igarashi et al.	
	4.	2003/0025 205	A1	02-06-2003	Shively et al.	
	5.	4,615,011		09-30-1986	Linsker	
	6.	4,673,966		06-16-1987	Shimoyama	
	7.	4,782,193		11-01-1988	Linsker	
	8.	5,360,948		11-01-1994	Thornberg	
	9.	5,618,744		04-08-1997	Suzuki et al.	
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	11.	5,650,653		07-22-1997	Rostoker et al.	
	12.	5,657,242		08-12-1997	Sekiyama et al.	
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	19.	6,128,767		10-03-2000	Chapman	
	20.	6,616,441		12-26-2000	Geryk	

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				Filing Date	1/31/02
				First Named Inventor	Steven Teig, et al.
				Group Art Unit	
				Examiner Name	
Sheet	2	of	4	Attorney Docket Number	SPLX.P0105

U.S. PATENT DOCUMENTS						
	21.	6,209,123	B1	03-27-2001	Maziasz et al.	
	22.	6,226,560	B1	05-01-2001	Hama	
	23.	6,262,487	B1	07-17-2001	Igarashi et al.	
	24.	6,301,686	B1	10-09-2001	Kikuchi et al.	
	25.	6,324,675	B1	11-27-2001	Dutta et al.	
	26.	6,342,682	B1	12-29-2002	Mori et al.	
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	29.	6,412,097	B1	06-25-2002	Kikuchi et al.	
	30.	6,436,804	B2	08-20-2002	Igarashi et al.	
	31.	6,512,292	B1	01-28-2003	Armburst et al.	
	32.	6,615,400	B1	09-02-2003	Lukanc	

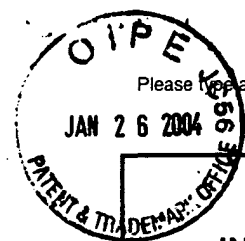
FOREIGN PATENT DOCUMENTS								
Examiner* Initials	Cite No. ¹	Foreign Patent Document			Date of Publication MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code (if known) ⁵				
	33.	JP	H03-173471	A	07-26-1991	Tawada et al.	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	√
	34.	JP	04000677	A	01-06-1992	Fujiwara		
	35.	JP	H05-102305	A	04-23-1993	Akihiro Sato	with Japanese Patent Office's English translation of Abstract;	√

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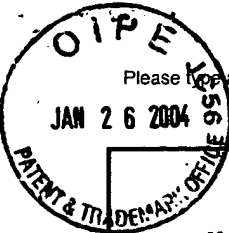
FOREIGN PATENT DOCUMENTS								
							and with English translation of the application.	
	36.	JP	H05-243379	A	09-21-1993	Masako Kubota	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	√
	37.	JP	H07-86407	A	03-31-1995	Shinpei Miura	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	√
	38.	JP	H09-162279	A	06-20-1997	Masaaki Yoshida	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	√

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				Group Art Unit	
				Examiner Name	
Sheet	4	of	4	Attorney Docket Number	SPLX.P0105

NON PATENT LITERATURE DOCUMENTS				
Examiner* Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ⁶
	39.	Berger, B. et al., Nearly Optimal Algorithms and Bounds for Multilayer Channel Routing, Journal of the Association for Computing Machinery, pp. 500-542, March 1995.		
	40.	Chen, H., Routing L-Shaped Channels in Nonslicing-Structure Placement. 24 th ACM-IEEE Design Automation Conference, pp. 152-165, 1987.		
	41.	Cheng, K. et al., Manhattan or Non Manhattan? A Study of Alternative VLSI Routing Architectures, pp 47-52, 2000.		
	42.	Chiang, C. et al., Wirability of Knock-Knee Layouts with 45° Wires, IEEE Transactions on Circuits and Systems, Vol. 38, Issue 6, pp 613-624, June 1991.		
	43.	Overtone, G., EDA Underwriter 2 Finding Space in a Multi-Layer Board, Electronic Engineering, Morgan-Grampian LTD, vol. 67, no. 819, pp 29-30.		

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